

I CLAIM:

1. A multi-development picture and picture-frame structure, comprising:

a picture-frame body, having a development window provided in the front and a multiplicity of inspecting window, a card reader port, and a multiplicity of selection push buttons provided on the backside therein;

a card reader, for accessing - image data in the memory card of a digital camera;

a memory, being a nonvolatile random access memory or flash memory, is used for storing the image data accessed from the card reader;

a microprocessor, connected to a multiplicity of selection buttons, the card reader, the memory and its driving circuit respectively for providing image selection signals and time setting for manipulating push buttons, as well as performing decoding, decrypting and sorting on the data stored in the memory in order to transform the data into display format;

a driving circuit, for driving the bistable display;

a bistable display, connected to the driving circuit for displaying the image data; and

a working power supply, being a battery, for providing the power source required by the whole control circuit and the bistable display;

whereby the digital image data accessed from the card reader can be

shown on the bistable display by selecting the variant frame and displaying time by employing the combination of the above-mentioned devices, and by employing the bistable display, the image can be stayed to constitute the displaying effect as a photo picture without continuous power supply .

- 5 2. The multi-development picture and picture-frame structure as claimed in claim 1, wherein the memory is provided with an image transformation program as well as an image staying and replacing program in order to transform the digital image data into the image displaying format, and the staying and replacing time of the image can be controlled by the program.
- 10 3. The multi-development picture and picture-frame structure as claimed in claim 1, wherein the microprocessor is further connected to a USB interface, and/or an infrared transmission interface, and/or a Bluetooth transmission interface, to receive the input data from various external storage devices and store them in the memory.
- 15 4. The multi-development picture and picture-frame structure as claimed in claim 1, wherein the microprocessor is further connected to an infrared transmission interface, and the input data are received from various external storage devices and stored in the memory through the infrared receiving device.
- 20 5. The multi-development picture and picture-frame structure as claimed in

claim 1, wherein the microprocessor is further connected to the Bluetooth transmission interface, and the input data are received from various external storage devices and stored in the memory through the Bluetooth receiving device.

- 5 6. The multi-development picture and picture-frame structure as claimed in claim 1, wherein the working power supply can be connected from the external power source transformer through the power source outlet for providing the power source required by the whole control circuit and the bistable display.